



Attorney Docket No. 614.1639D2DDD1

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shinpei-KOMATSU, et al.

Application No.: 10/785,010

ATTENTION

Confirmation No.: 9725

CERTIFICATE OF CORRECTION

Filed: February 25, 2004

BRANCH

U.S. Patent No.: 7,188,210

Issued: March 6, 2007

For: METHOD OF WRITING, ERASING, AND CONTROLLING MEMORY FOR MEMORY  
DEVICE

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the  
subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s)  
shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

June 13, 2007

By:

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**Certificate**  
**JUN 19 2007**  
**of Correction**

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

PATENT NO : 7,188,210

DATED : March 6, 2007

INVENTOR(S) : Shinpei KOMATSU, et al.

First Page, Column 1 (Related U.S. Application Data), Line 3, change "08/688,858,"  
to --09/688,858,--.

Column 26, Line 53, change "comrpising:" to --comprising:--.

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PATENT NO. 7,188,210

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